

NATIONAL CONFERENCE ON EMERGING TRENDS IN ELECTRONICS (ETE-2026)

4th & 5th February, 2026



Organized by
DEPARTMENT OF ELECTRONICS



University of Jammu

BABA SAHEB AMBEDKAR ROAD, NEW CAMPUS,
JAMMU-180006(INDIA)

ABOUT THE UNIVERSITY OF JAMMU

The University of Jammu, accredited as 'A++' Grade University by NAAC of India came into existence in 1969 vide Kashmir and Jammu Universities Act 1969 following bifurcation of the erstwhile University of Jammu and Kashmir. The University provides instructions in various branches of learning as it deems fit and makes provision for research and the advancement and dissemination of knowledge. The University stands for spiritual and material elements in life thrust for knowledge and virtue under the backdrop of holy peaks of Trikuta Hills.

ABOUT THE DEPARTMENT

Department of Electronics became an independent department in 2017, after bifurcation of Department of Physics and Electronics. The post graduate programme in Electronics was however being offered in the Department of Physics & Electronics since the year 1995. The department is presently offering Ph.D. and M.Sc. in Electronics. The department apart from teaching is actively engaged in research in various technologies and allied sciences such as Fabrication and Characterization of Thin Film based Devices, Gate Stacks using High-k Dielectrics, Hybrid Solar Cells, GaN based LEDs, Device and Circuit Simulation, Low Voltage Circuit Design, Speech Signal Processing, Machine Learning based Automation, IoT based Systems, Smart Sensor Design, Energy Efficient Systems and Digital Signal Processing.

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Prof. Sumeet Gupta, SMVDU, Katra
Dr. Farooq A Khanday, University of Kashmir

OBJECTIVES OF THE CONFERENCE

- To showcase research developments in emerging areas of electronic technologies and allied sciences.
- To provide a platform for scientists, researchers, scholars, and students to share their research findings and experiences.
- To encourage collaboration between researchers and institutions to improve the quality of research publications.
- To update participants with current developments in the field of electronic technologies.
- To foster a conducive academic and research ecosystem, especially for young researchers to interact with eminent invited speakers and collaborate on contemporary research.

THRUST AREAS (But not limited to)

- Nano-Science and Nanotechnology
- Nano-energy & Nano-Biosensors
- Lab-on-chip
- Smart Materials and Characterization Techniques
- Circuit design & Simulation
- Photovoltaics
- Thin film solar cells
- Signal processing
- Modelling of Devices & Circuits
- Biomedical applications
- Internet of Things (IoT)
- Artificial Intelligence (AI)

REGISTRATION DETAILS

Delegates/Faculty members: Rs. 1,500/-

Research Scholars: Rs. 750/-

Students: Rs. 500/-

For fee submission scan the QR code. The registration fee will be accepted through online mode only.

NOTE: PAID ACCOMMODATION AT AFFORDABLE RATES SHALL BE PROVIDED TO ONLY THOSE OUTSTATION PARTICIPANTS WHO WILL INTIMATE THE ORGANIZERS WELL IN ADVANCE.

IMPORTANT DATES

- Call for Abstracts: 1st January 2026 onwards
- Abstract submission deadline: 25th January 2026
- Acceptance of Abstracts: 26th January 2026
- Registration starts: 26th January 2026
- Registration closed: 3rd February 2026
- On spot Registration: 4th February 2026

Abstract Format

Abstract should be limited to 250-300 words typed in Times New Roman, 12-point font, single spacing. The abstract must include: Title (Bold, 14-point)

Author(s) name(s) and affiliation(s) (12-point)

E-mail address of corresponding author

Keywords (3-5). The abstract in specified format should be mailed at ete2025electronicsju@gmail.com

Guidelines for Preparing Poster

Poster size must be in one piece of 1 x 1 meter. (Avoid A4 size papers).

CONTACT DETAILS

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